

ABSTRACT**A protocol structure to accelerate memory transmission**

The present invention provides a protocol structure to accelerate memory transmission, characterized in that an address memory unit is built in the front end of the memory, of which comprises several address buffers, and there is a pointing design between
5 each address buffer and the corresponding data block in the memory. Address data is saved in address buffers in advance by external pins to further quickly match such address data with the corresponding data block without entering long data address every time. The invention provides the industry users the method to rapidly change the data address for data
10 transmission and access.